LAYOUT DIAGRAM 原理图:(FIG-1)

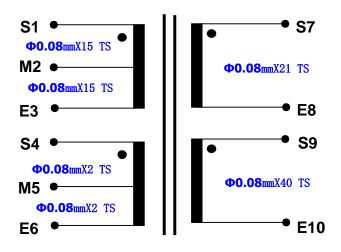
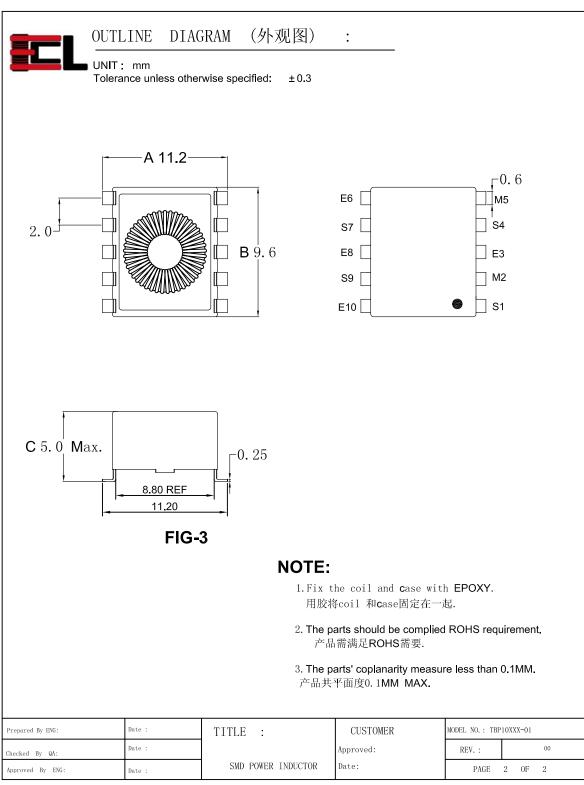


FIG-1

DATA SPECIFICATION (特性要求): (TABLE-1)

ITEM 项目	MEASUREMENTS 测试脚位	CHARACTERISTICS 测 试 范围	MEASURING CONDITIONS 测试条件 T=25℃±3;HR(%)=45±15	REMARK 备注		
LS 電感	PIN(S1-E3)	2. OmH +40%/-35%	F(kHz)=100 Ug(V)=1	SERIAL MODE 串联模式		
	PIN(S4-E6)	38uH +40%/-35%	F(kHz)=100 Ug(V)=1	SERIAL MODE 串联模式		
	PIN(S7-E8)	1.0mH +40%/-35%	F(kHz)=100 Ug(V)=1	SERIAL MODE 串联模式		
	PIN(S9-E10)	3.6mH +40%/-35%	F(kHz)=100 Ug(V)=1	SERIAL MODE 串联模式		
COPLANARI TY 共平面度	ALL PINS	O. 1mmMAX				
PH 相位	REFER TO LAYOUT DIAGRAM (FIG-1) 参考原理图(FIG-1)					

Prepared By ENG:	Date:	TITLE:	CUSTOMER	MODEL NO.:TBP10XXX-01	
Checked By QA:	Date:		Approved:	REV.	00
Approved By ENG:	Date:	SMD POWER INDUCTOR	Date:	PAGE	1 OF 2



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