

LAYOUT DIAGRAM(原理图): (FIG-1)

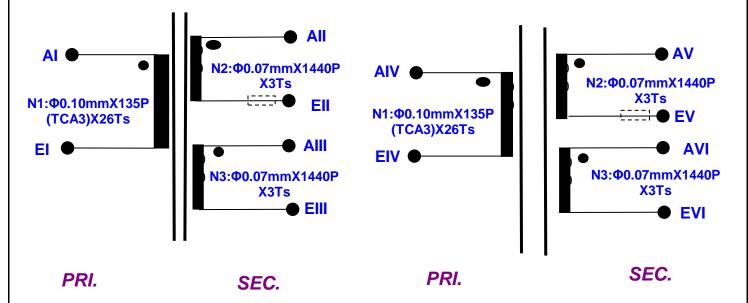


FIG-1

Remark:

1." ____ "Wrap the tail wire with 1PEN2 tape to prevent short circuit

1PEN2 胶纸包住尾线,防止短路.

CROSS SECTION DAGRAM剖视图: (FIG-2)

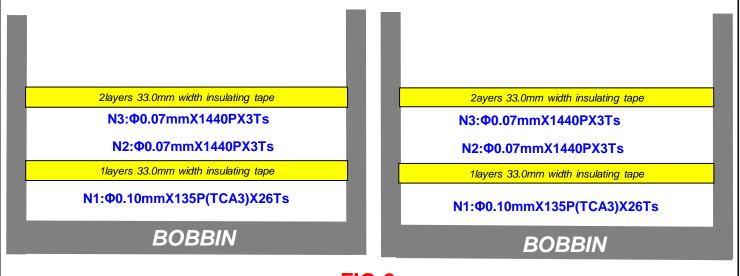


FIG-2

Prepared By:	DATE :	TITLE:	CUSTOMER	MODEL NO.:PQ5050	
Checked By:	DATE :		Approved:	REV.:	01
Approved By:	DATE :	TRANSFORMER	Date:	PAGE 1 OF 3	



ITEM 项目	MEASURMENT S 测试脚位	CHARACTERISTICS 测试范围	MEASURING CONDITIONS 测 试 条 件 (T=20±4℃;HR(%)=45±15)	REMARK 备注		
LS 電 感	PIN(AI- EI)/PIN(AIV-EIV)	350uH±20%	F(kHz)=10 Ug(V)=0.1	SERIAL MODE 串联模式		
HIPOT 耐压	PRI./SEC.	NO BREAKDOWN 漏 电 流≤5mA	VAC=3200V/2SEC. F(Hz)=50/60	/		
	COIL./COVER	NO BREAKDOWN 漏 电 流≤5mA	VAC=1600V/2SEC. F(Hz)=50/60	/		
PH 相 位	REFER TO LAYOUT DIAGRAM FIG-1 参照原理图 FIG-1					

REMARK:初级和次级不相同的两个绕组需测试是否短路

100% testing for the above electrical characteristics.

所有产品按以上要求进行100%测试.

Nominal current: leff = 10A, lmax = 30A 标称电流: leff = 10A, lmax = 30A

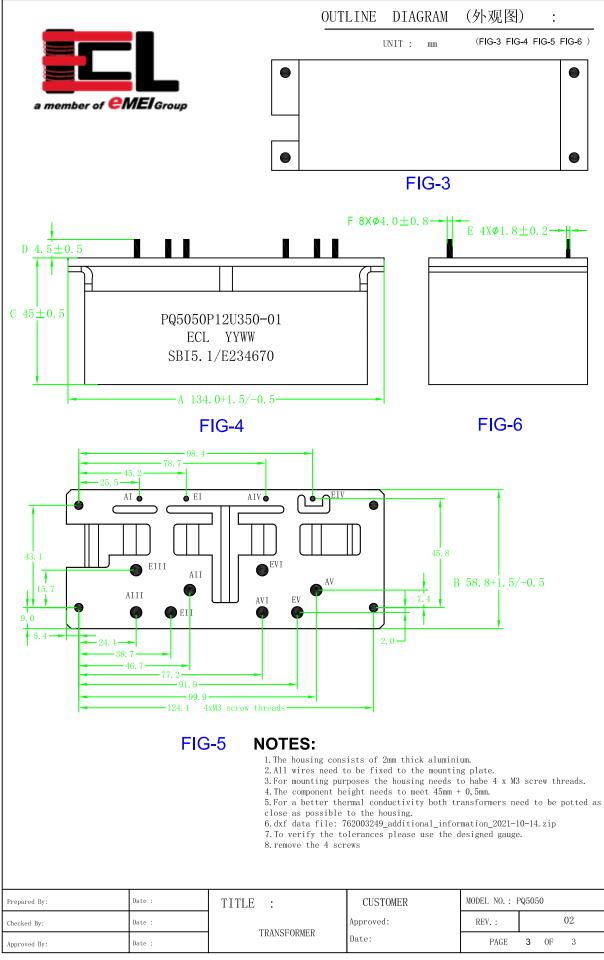
Operating - / Storage temperature: -25° C - +125° C /-25° C - +85° C

工作/储存温度: -25° C - +125° C /-25° C - +85° C

Quality Requirement:(品质要求)

Ref ANSI/ASQC.Z1.4 2008 Level II. Major AQL=0.4% Minor AQL=1.0%. 参照ANSI/ASQC.Z1.4 2008 Level II. Major AQL=0.4% Minor AQL=1.0%.

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