



## DATA SPECIFICATION特性要求: (Table-1)

ITEM 项目	MEASURMENTS 测试脚位	CHARACTERISTI CS 测试范围	MEASURING CONDITIONS \测 试 条 件 (T=25±4℃;HR(%)=45±15)	REMARK 备注			
Ls 電 感	PIN(2-4)	850uH±10%	F(kHz)=100 Ug(V)=1	SERIAL MODE 串联模式			
Lk 漏 感	PIN(2-4)	25.0uH MAX.	F(kHz)=100 Ug(V)=1 Short all other pins	SERIAL MODE 串联模式			
HIPOT 耐压	PRI./SEC.	NO BREAKDOWN 漏电流≤5.0mA	VAC=500V /3SEC. F(Hz) =50	/			
	PRI./CORE.	NO BREAKDOWN 漏电流≤5.0mA	VAC=500V /3SEC. F(Hz) =50	/			
	SEC./CORE.	NO BREAKDOWN 漏电流≤5.0mA	VAC=500V /3SEC. F(Hz) =50	/			
TR 圏比	PIN(2-4):(8-7)	174Ts:25Ts±0.5Ts	F(kHz)=10 Ug(V)=0.1	以平面磁芯 测试为准			
	PIN(2-4):(7-6)	174Ts:30Ts±0.5Ts	F(kHz)=10 Ug(V)=0.1	以平面磁芯 测试为准			
COPLANA RITY 共平面度	ALL	PINS	0.15mm MAX.	/			
PH 相位	REFER TO LAYOUT DIAGRAM FIG-1 参照原理图 FIG-1						

REMARK: 初级和次级不相同的两个绕组需测试是否短路

100% testing for the above electrical characteristics. 所有产品按以上要求进行100%测试.

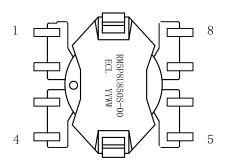
## Quality Requirement:(品质要求)

Ref ANSI/ASQC.Z1.4 2008 Level II. Major AQL=0.4% Minor AQL=1.0%. 参照ANSI/ASQC.Z1.4 2008 Level II. Major AQL=0.4% Minor AQL=1.0%.

Prepared By:	Date:	TITLE:	CUSTOMER	MODEL NO.: RM5	
Checked By:	Date:	IIILE.	Approved:	REV.	00
Approved By:	Date:	TRANSFORMER	Date:	PAGE 2 OF 3	

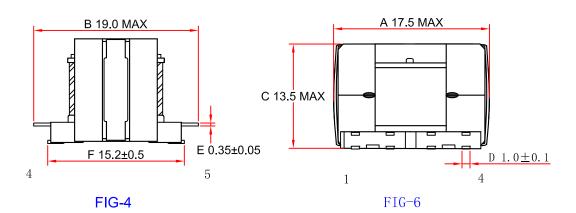
# OUTLINE DIAGRAM (外观图)

UNIT: mm (FIG-3 FIG-4 FIG-5 FIG-6)



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FIG-3



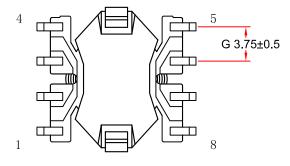


FIG-5

### NOTES:

- 1. The product must be impregnated in varnish 产品须浸凡立水
- 2. Fix tow cores with epoxy E-500 磁芯点E-500胶固定
- 3. YYWW: year and week code(FIG-3). YYWW:年份周期代码(FIG-3).
- 4. The parts must be complied with ROHS requirement. 产品需满足ROHS需要.

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