

LAYOUT DIAGRAM 原理图:(FIG-1)

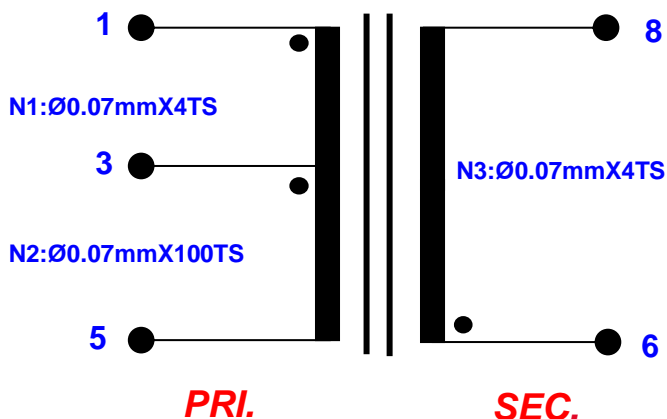


FIG-1

CROSS SECTION DIAGRAM 剖视图 : (FIG-2)



FIG-2

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DATA SPECIFICATION(特性要求): (TABLE-1)

ITEM 项目	MEASUREMENTS 测试脚位	CHARACTERISTICS 测试范围	MEASURING CONDITIONS 测试条件 T=23± 4℃;HR(%)=45± 15	REMARK 备注
LS 电感	PIN(1-5)	1.7mH±10%	F(kHz)=1 Ug(V)=0.25	SERIAL MODE 串联模式
DCR 直流电阻	PIN(1-3)	430mΩMAX.	/	SERIAL MODE 串联模式
	PIN(3-5)	10.2ΩMAX.	/	SERIAL MODE 串联模式
	PIN(6-8)	470mΩMAX.	/	SERIAL MODE 串联模式
TR 圈比	PIN(1-3):(3-5):(6-8)	4:100:4±0.5Ts	F(kHz) =20 Ug(V) =1.0	以平面磁芯 测试为准
HIPOT 耐压	PRI./ SEC.	NO Breakdown 漏电流≤5.0mA	VAC=500V/3SEC. F(Hz)=50	/
	COIL/CORE	NO Breakdown 漏电流≤5.0mA	VAC=500V/3SEC. F(Hz)=50	/
PH 相位	REFER TO LAYOUT DIAGRAM FIG-1 参考原理图(FIG-1)			

REMARK:

100% testing for the above electrical characteristics.
所有产品按以上要求进行100%测试.

Quality Requirement:(品质要求)

Ref ANSI/ASQC.Z1.4 2008 Level II . Major AQL=0.4% Minor AQL=1.0%.
参照ANSI/ASQC.Z1.4 2008 Level II . Major AQL=0.4% Minor AQL=1.0%.

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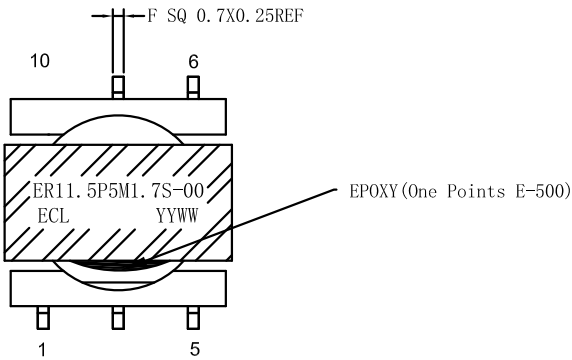


FIG-3

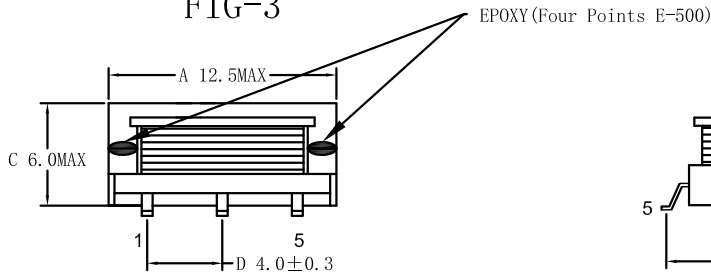


FIG-4

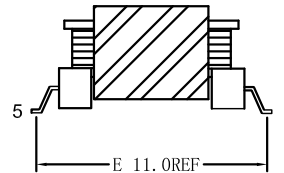


FIG-6

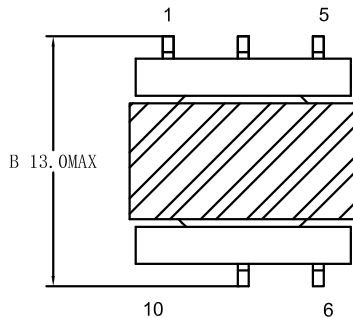


FIG-5

NOTES:

1. REMOVE PIN 2, 4, 7, 9, 10.
拔除PIN2, 4, 7, 9, 10.
2. Fixed the cores with epoxy E-500.
使用E-500胶固定两铁芯.
3. Fix two cores with 2 Layers tape of 6.0mm.
铁芯用6.0mm胶带包2圈固定.
4. YYWW: year and week code(FIG-3).
YYWW:表示年份和周期代码(FIG-3).
5. The product should be according to RoHS
产品要符合RoHS
6. The parts' coplanarity measure less than 0.10MM MAX.
产品共平面度0.10MM MAX.

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