

LAYOUT DIAGRAM 原理图:(FIG-1)

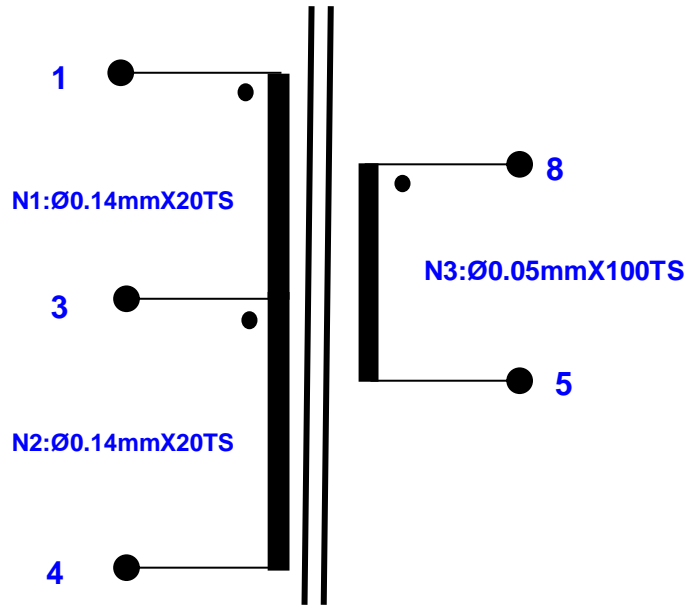


FIG-1

CROSS SECTION DIAGRAM 剖视图 : (FIG-2)

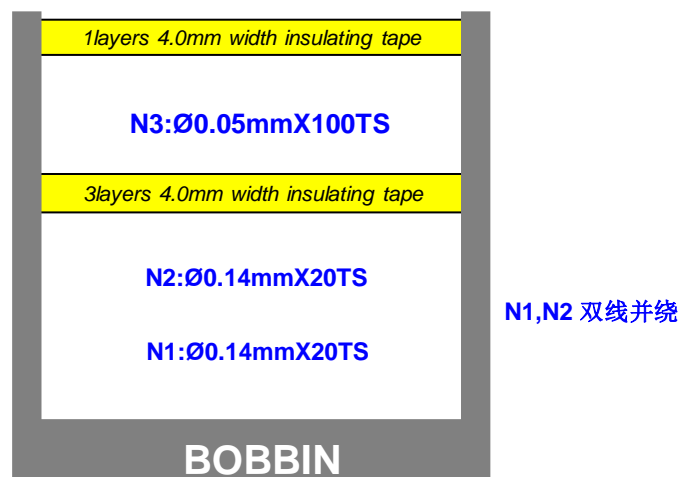


FIG-2

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CHECKING SPECIFICATION(特性要求): (Table-1)

ITEM 項目	MEASUREMENTS 測試腳位	CHARACTERISTICS 測試範圍	MEASURING CONDITIONS 測試條件 T(°C)=22±3; HR(%)=45±	REMARK 備注
LS 电感	PIN(8-5)	8.2mH±35%	F(KHz) =10 U(V) =0.05	SERIAL MODE 串联模式
DC.R 直流电阻	PIN(1-3)	0.47Ω MAX	25°C	SERIAL MODE 串联模式
	PIN(1-4)	0.93Ω MAX	25°C	SERIAL MODE 串联模式
	PIN(8-5)	22.0Ω MAX	25°C	SERIAL MODE 串联模式
HI-POT 高壓	PRI./SEC.	NO Breakdown 漏电流 ≤5mA	VAC=500V /3SEC. F(Hz) =50/60Hz	/
COPLANARITY 共平面度	ALL PINS	0.15mm MAX.	/	/
PH 相位	REFER TO LAYOUT DIAGRAM FIG-1 参考原理图 (FIG-1)			

REMARK:

100% testing for the above electrical characteristics.

所有产品按以上要求进行100%测试.

Quality Requirement:(品质要求)

Ref ANSI/ASQC.Z1.4 2008 Level II . Major AQL=0.4% Minor AQL=1.0%.

参照ANSI/ASQC.Z1.4 2008 Level II . Major AQL=0.4% Minor AQL=1.0%.

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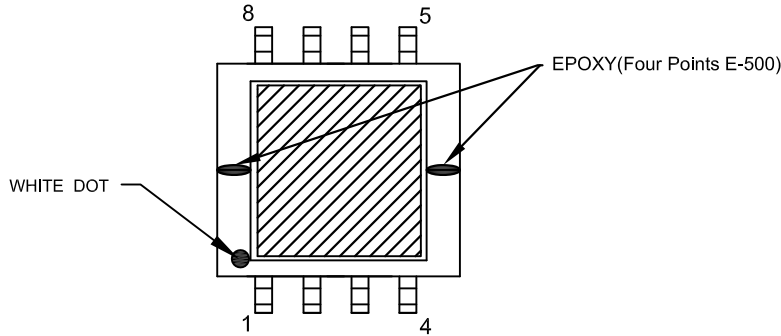


FIG-3

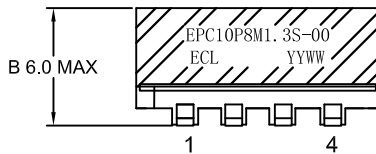


FIG-4

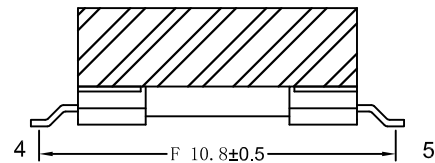


FIG-6

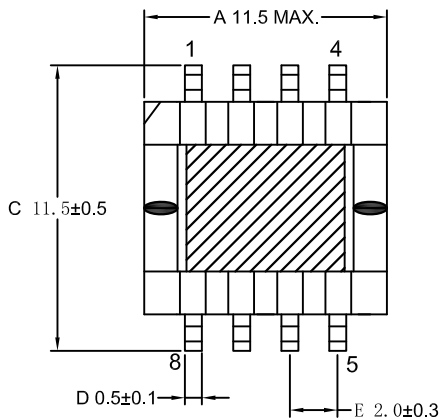


FIG-5

NOTE:

1. The parts' coplanarity measure less than 0.15mm.
产品平面度0.15mm MAX.
2. The cores with 2 layers tape of 3.5mm,
磁芯用3.5mm胶带包2圈固定
3. Fix two cores with epoxy E-500.
使用E-500胶固定两磁芯.
4. YYWW: year and week code (FIG-4).
YYWW: 年份周期代码 (FIG-4).
5. The part must be complied with RoHS.
产品需满足RoHS要求.

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