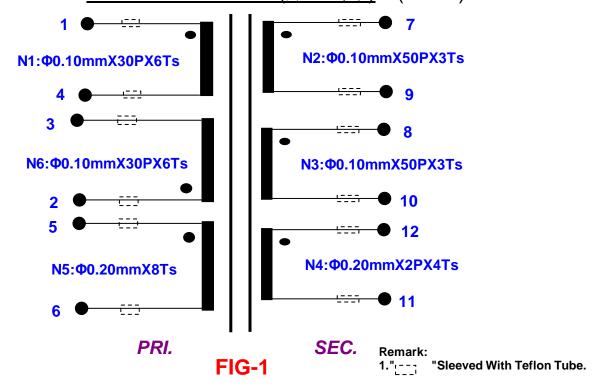


LAYOUT DIAGRAM(原理图): (FIG-1)



CROSS SECTION DAGRAM剖视图: (FIG-2)

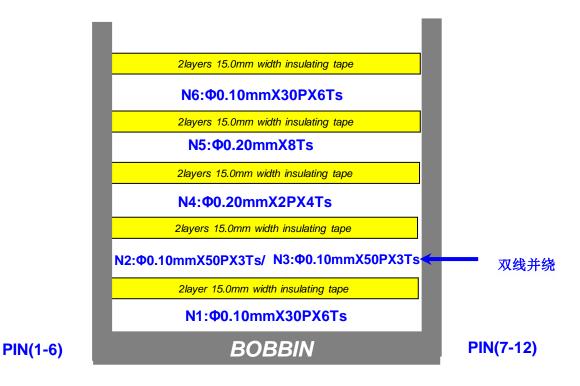


FIG-2

Prepared By:	DATE :	TITLE:	CUSTOMER	MODEL NO.:EFD20	
Checked By:	DATE :		Approved:	REV.:	00
Approved By:	DATE :	TRANSFORMER	Date:	PAGE 1 OF 3	



DATA SPECIFICATION特性要求: (Table-1)

ITEM 项目	MEASURMENT S 测试脚位	CHARACTERISTICS 测试范围	MEASURING CONDITIONS 测试条件 (T=25±4℃;HR(%)=45±15)	REMARK 备注
LS 電 感	PIN(1-4)	40.0uH MIN	F(kHz)=100 Ug(V)=1	SERIAL MODE 串联模式
DCR 直流电阻	PIN(1-4)	24.0mΩ MAX	20℃	4 WIRES MODE 四线模式
TURN RATIO 圈 数 比	N1:N2:N3:N4:N5 :N6	6Ts:3Ts:3Ts:4Ts:8Ts:6T s±0.5Ts	F(kHz)=20 Ug(V)=1.0	以平面磁芯 测试为准
COPLANAR ITY 共同平面度	ALL PINS	0.1mm MAX	/	
HIPOT 耐压	PRI./SEC.	NO BREAKDOWN 漏 电 流≤5.0mA	VAC=1500V/60SEC. F(Hz)=50	/
	N1,N6./CORE.	NO BREAKDOWN 漏 电 流≤5.0mA	VAC=500V/60SEC. F(Hz)=50	/
PH 相 位	REFER TO LAYOUT DIAGRAM FIG-1 参照原理图 FIG-1			

REMARK:初级和次级不相同的两个绕组需测试是否短路

100% testing for the above electrical characteristics. 所有产品按以上要求进行100%测试.

Quality Requirement:(品质要求)

Ref ANSI/ASQC.Z1.4 2008 Level II. Major AQL=0.4% Minor AQL=1.0%. 参照ANSI/ASQC.Z1.4 2008 Level II. Major AQL=0.4% Minor AQL=1.0%.

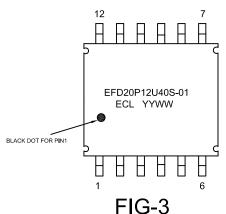
Prepared By:	DATE :	TITLE:	CUSTOMER	MODEL NO.:EFD20	
Checked By:	DATE :		Approved:	REV.:	00
Approved By:	DATE :	TRANSFORMER	R Date:	PAGE 2 OF 3	



OUTLINE DIAGRAM (外观图)

UNIT: mm

(FIG-3 FIG-4 FIG-5 FIG-6)



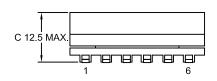


FIG-4

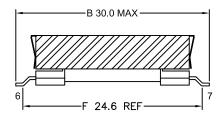
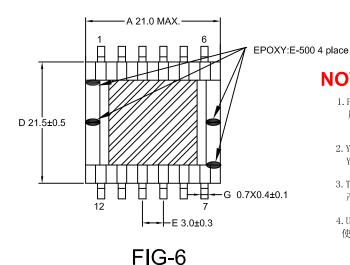


FIG-5



NOTES:

- 1. Fix the core and bobbin with clip . 用铁夹将磁芯和胶芯固定在一起.
- 2.YYWW: year week code(FIG-3). YYWW:周期年份代码(FIG-3).
- 3. The product should be according to RoHS 产品要符合RoHS
- 4. Using glue (E 500) fixed magnetic core and bobbin. 使用胶水 (E-500) 固定磁芯与骨架.

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Prepared By:	Data:	TITLE:	CUSTOMER	MODELNO: EFD20
Checked By:	Data:		Approved:	REV: 00
Approved By:	Date:	TRANSFORMER	Date:	PAGE 3 OF 3

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