

LAYOUT DIAGRAM(原理圖): (FIG-1)

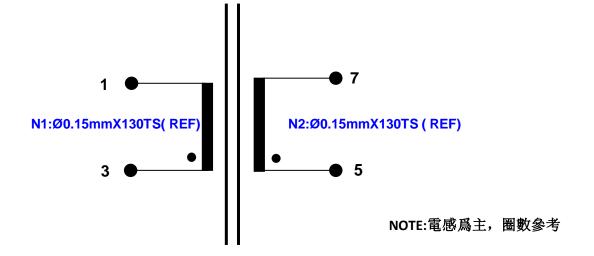
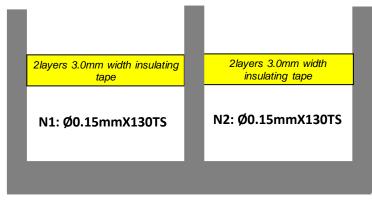


FIG-1

CROSS SECTION DIAGRAM剖视图: (FIG-2)



PIN1-3 SIDE PIN4-8 SIDE

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<u>Checking Specification(特性要求)</u>: (Table-1)

ITEM 項 目	MEASUREMENTS 测试腳位	CHARACTERISTICS 测 试 範 圍	MEASURING CONDITIONS 測試條 件 T(℃)=25±3;HR(%)=45±15	REMARK 備 注		
LS 电 感	PIN(3-1) PIN(5-7)	40.0mH MIN	F(kHz) = 1 Ug(V) = 0.25	SERIAL MODE 串联模式		
DCR 直流电阻	PIN(3-1) PIN(5-7)	5.0Ω MAX	25℃	4 WIRES MODE 四线模式		
TR 圏比	(3-1):(5-7)	1:1±0.02	F(kHz)=20 Ug(V)=1	以平面磁芯 測試爲准		
inductance difference 电感差别	PIN(3-1) PIN(5-7)	±3%	/			
HIPOT 耐压	COIL/COIL	NO BREAKDOWN 漏 电 流≤5.0 mA	1000Vac/3SEC F(Hz) =60	/		
	COIL/CORE	NO BREAKDOWN 漏 电 流≤5.0 mA	500Vac/3SEC F(Hz) =60	/		
PH 相 位	REFER TO LAYOUT DIAGRAM (FIG-1)					

REMARK:

100% testing for the above electrical characteristics. 所有产品按以上要求进行100%测试.

Quality Requirement:(品质要求)

Ref ANSI/ASQC. Z1. 4 2008 Level II . Major AQL=0. 4% Minor AQL=1. 0%. 参照ANSI/ASQC. Z1. 4 2008 Level II . Major AQL=0. 4% Minor AQL=1. 0%.

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(外观图) OUTLINE DIAGRAM UNIT: mm (FIG-3 FIG-4 FIG-5 FIG-6) TOLERANCE: .X ±0.3mm .XX±0.1mm a member of **CMEI** Group 8 7 9 4 EE12.6P8 M40S-00 **ECL YYWW** WHITE POINT FOR PIN1 白点 标示1脚位 3.2 FIG-3 D 2.54±0.3 A 18.5MAX FIG-4 FIG-6 B 14.5 MAX **NOTES:** 1.Between the core and dobbin glue fixed point E - 500 磁芯與BOBBIN之間點E-500膠水固定 2.YY/WW:year and week code;(FIG-3) YY/WW:表示年份/周期. H H 6 5 FIG-5 Date: CUSTOMER Prepared By: MODEL NO.: EE12.6 Date: TITLE: Approved: Checked By: REV.: 00 PAGE: 3 OF 3 Approved By: Date: **TRANSFORMER** Date:

免责条款

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